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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10050104	FILING DATE 01/18/2002	CLASS 438	SUBCLASS 217 ?	GAU 2812	EXAMINER Bratlie
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\*\*CONTINUING DATA VERIFIED:

\*\* FOREIGN APPLICATIONS VERIFIED:

REPUBLIC OF KOREA 2001-25078 05/09/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		SEC.890
Verified and Acknowledged Examiners's initials		
TITLE : Self-contained semiconductor device manufacturing equipment having compact arrangement of load-lock and processing chambers		

U.S. DEPT. OF COMM. / PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
Assistant Examiner		Total Claims	Print Claim for O.G.
Primary Examiner		DRAWING	
PREPARED FOR ISSUE		Sheets Drawg.	Figs. Drawg.
Application Examiner		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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